

REMARKS/ARGUMENTS

The foregoing amendment and the following arguments are provided to impart precision to the claims, by more particularly pointing out the invention, rather than to avoid prior art.

35 U.S.C. §102(e) Rejections

Examiner rejected claims 1-16 under 35 U.S.C. §102(b) as being anticipated by U.S. Patent 6,513,109 (hereinafter "Gschwind"). Notably, it is asserted in the Office Action that Gschwind teaches all claimed limitations within Independent claims 1, 9, and 14.

Presently amended claims 1 and 14 now include a unit to insert a micro-operation into an instruction stream, which defers renaming of a plurality of registers defined by different predicates upon which a dependent or common instruction depends. Furthermore, claims 1 and 14 more clearly claim the limitation related to the register renaming units to rename a predicated instruction after the dependent/common instruction is executed.

Gschwind, on the other hand, does not teach inserting a micro-operation into an instruction stream to defer renaming of a plurality of registers defined by different predicates upon which a dependent or common instruction depends. Instead, Gschwind teaches two relevant techniques: 1) Predicting the result of the predicate within the predicated instruction such that the execution of dependent instructions are not delayed and renaming registers corresponding the predicated instruction accordingly (Col. 5, Lns. 40-43); and 2) Executing the predicated instructions regardless of whether the predicate has been resolved by choosing a predicate value and

therefore renaming registers corresponding to the predicated instruction and correct any mistakes in choosing the predicate value later in the pipeline (Col. 5, Lns. 20-33).

Claims 1 and 14 as presently amended are not anticipated by Gschwind. Accordingly, Applicant respectfully submits that claims 1 and 14 and all claims depending therefrom are in condition for allowance.

Claim 9 claims, among other things, assigning a renamed destination register for each one of a plurality of predicated instructions with a corresponding predicate register to corresponding source operands within the select-uop. Gschwind teaches no such select-uop or any other means that performs the claimed function of the select-uop of claim 9. Furthermore, Applicant is unable to glean any relevance to the cited portions of Gschwind in the rejection of claims 9 pertaining to the select-uop. Neither Figures 10-11, column 8 lines 25-43, nor column 9 line 65-column 10 line 32, as cited in the Office Action, teach any sort of uop whose source operands correspond to renamed registers, as claimed in claim 9.

Instead, the cited references in Gschwind teach that execution suppression can involve the delaying register renaming until the predicate value is predicted. However, nothing in the cited references to Gschwind nor any other portion of Gschwind teaches a renaming scheme that uses a select-uop whose source operands correspond to renamed registers that correspond to unresolved predicate values, as claimed in claim 9.

Therefore, Gschwind does not anticipate claim 9, as asserted in the Office Action. Accordingly, Applicant respectfully submits that claim 9 and all claims depending therefrom are in condition for allowance.

Because the other rejected claims depend from at least one of the presently amended independent claims, and because it is believed by Applicant that the presently amended independent claims are in condition for allowance, the dependent claims are also accordingly in condition for allowance.

Accordingly, Applicant respectfully submits the present application is in condition for allowance. If the Examiner believes a telephone conference would expedite or assist in the allowance of the present application, the Examiner is invited to call Erik Metzger at (512) 732-3922.

If any additional fee is required, please charge Deposit Account No. 02-2666.

Respectfully submitted,

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